

# Call for Participants

## 2026 Intel Cup Undergraduate Electronic Design Contest -Embedded System Design Contest

Website: <http://nuedc.sjtu.edu.cn>

(only valid for teams from Chinese HK SAR and overseas)

2026 Intel Cup Undergraduate Electronic Design Contest-Embedded System Design Contest (Intel Cup Embedded System Design Contest: Intel Cup ESDC), is initiated by PRC Ministry of Education and Ministry of Industry and Information Technology, hosted by Shanghai Jiao Tong University, and solely sponsored by Intel Corporation since 2002. This biannual contest provides an opportunity for top undergraduate students to design an innovational working system based on an assigned Intel embedded platform over a period of three months. Each team consists of no more than three undergraduate students, plus a faculty mentor.

To empower undergraduates with critical technology and social skills that are essential to thrive in the digital economy, and provide an international platform for students to communicate and learn from each other, **2026 Intel Cup Embedded System Design Contest is now calling for international participants, one to two teams from each university invited.** All participant teams get latest embedded hardware platforms and related software empowered by Intel.

### 1. Key Dates

- Registration due: March 15, 2026 for confirming team engagement and May 31, 2024 for team member information registration
- Contest period: March 31~June 30, 2026
- Project Topic submission deadline: May 31, 2026
- Final reports submission deadline: July 1, 2026
- Appraisal period: July 20~July 25, 2026

### 2. Eligibility

This contest only opens to universities invited by contest committee. Every participant should be actively enrolled as a student in an accredited college/university during the contest. Each team should consist of no more than three undergraduate students, and a faculty mentor.

### 3. Platform

The theme of 2026 ESDC contest is: AI application and innovation.

Each participant team will be provided an embedded platform as mandatory board with the corresponding Intel software. All teams must develop systems on the assigned board. The detail information about the mandatory board will be announced on ESDC official website.

## **4. Timetable**

### **1) Registration**

The registration includes two stages:

- Registration of university contact person and the number of teams participating in the contest. Universities should complete the ‘First Stage Registration Form’ and send the scanned copy by email to [nuedc@sjtu.edu.cn](mailto:nuedc@sjtu.edu.cn) by March 15, 2026. This form can be downloaded from website: <http://nuedc.sjtu.edu.cn>.
- Registration of detailed team member information. The detailed information of team members (including names, university, etc.) needs to be registered before May 31, 2026 according to the registration information on website <http://nuedc.sjtu.edu.cn>. After registration, a scanned copy of the registration information with team members’ signatures should be sent back to the organizing committee by email ([nuedc@sjtu.edu.cn](mailto:nuedc@sjtu.edu.cn)) before May 31, 2026.

### **2) Training**

Training workshops for contest hardware and software platforms will be held online via live broadcast.

Detail training dates and resources will be announced on ESDC website: <http://nuedc.sjtu.edu.cn>.

Contest hardware platforms will be delivered to all participant teams on April 1, 2026.

### **3) Appraisals**

Each team should submit the project topic before May 31, 2026 to the organizing committee.

Contest project due date is June 30, 2026. All participant teams should submit all necessary contest materials to the organizing committee on time, including: a brief introduction, a full report in English and a working system. The working language of

the contest is English and Chinese.

- Brief introduction. Introduction template can be downloaded from ESDC website.
- Full report.  
For teams from Chinese Hong Kong SAR, and overseas, the full report in English is necessary, and the report should contain the following information: declaration of originality (template to be downloaded from website), title, abstract (within 120 words in English) and keywords, system schema, functions, and implementation, test plans and result analysis, and references. The report must be within 20 pages in English (font size 12, and single line spacing), including all the information required.

Teams from Chinese Hong Kong SAR, and overseas can also submit the full report in Chinese. If teams submit full report in Chinese, a detailed abstract in English is necessary within 6 pages (font size 12, single line spacing), including English title, abstract, system schema and implementation, test results.

- Working system. The working system should be built on the assigned platform.

All teams are required to submit the brief introduction and full report before July 1<sup>st</sup>, 2026 by email, while the working system should be kept by the teams for appraisal. Details on submission will be announced in early June, 2026 on ESDC website.

The appraisal will be held on July 20~25, 2026. All participant teams are required to attend the judgment. In general, each team has no more than 30 min to for presentation and demonstration, including oral presentation (PPT), test for the working system and Q&A.

More details about the appraisal will be announced on ESDC website in June, 2026.

#### **4) Awarding ceremony**

Awarding ceremony details will be announced in July, 2026.

#### **5) Others**

Please refer to the regulation of the contest on website for more information.

**For more information about the contest, please contact:**

Prof. Jiang Letian, and Cui Meng of the Contest Committee

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Organizing Committee of Intel Cup Undergraduate Electronic Design Contest  
-Embedded System Design Invitational Contest  
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